



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James Smith et al.

Title: IDENTIFYING PROGRAM PHASE CHANGES THROUGH PROGRAM WORKING SET ANALYSIS

Docket No.: 800.035US1

Filed: February 5, 2004

Examiner: Unknown

Serial No.: 10/772,555

Due Date: N/A

Group Art Unit: 2122

MS Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- Return postcard.
 Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 5 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

Customer Number 21186

By: Ann M. McCrackin
Atty: Ann M. McCrackin
Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 3rd day of March, 2006.

James Kausik
Name

James Kausik
Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)



S/N 10/772,555

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: James Smith et al. Examiner: Unknown
Serial No.: 10/772,555 Group Art Unit: 2122
Filed: February 05, 2004 Docket: 800.035US1
Title: IDENTIFYING PROGRAM PHASE CHANGES THROUGH PROGRAM
WORKING SET ANALYSIS

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No :10/772,555

Filing Date: February 05, 2004

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Page 2
Dkt: 800.035US1

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

JAMES SMITH ET AL.

By their Representatives,

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Date March 2, 2006

By Ann M. McCrackin

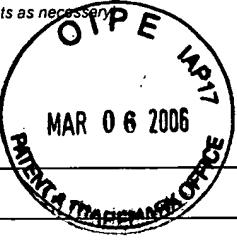
Ann M. McCrackin

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JAMES KAWYISK
Name

James Kawayisk
Signature

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		<i>Complete if Known</i> Application Number 10/772,555 Filing Date February 5, 2004 First Named Inventor Smith, James Group Art Unit 2122 Examiner Name Unknown	
 Sheet 1 of 1		Attorney Docket No: 800.035US1	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		BALASUBRAMONIAN, RAJEEV, et al., "Memory Hierarchy Reconfiguration for energy and Performance in General-Purpose Processor Architectures", <u>Proceedings of the 33rd Annual ACM/IEEE International Symposium on Microarchitecture</u> , (2000), 245-257	
		HUANG, MICHAEL C., et al., "Positional Adaptation of Processors: Application to Energy Reduction", <u>International Symposium on Computer Architecture</u> , (2003), 1-12	
		SHERWOOD, TIMOTHY, et al., "Automatically Characterizing Large Scale Program Behavior", <u>10th International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS X)</u> , (October, 2002), 1-13	
		SHERWOOD, TIMOTHY, et al., "Basic Block Distribution Analysis to Find Periodic Behavior and Simulation Points in Applications", <u>Proceedings of the International Conference on Parallel Architectures and Compilation Techniques (PACT)</u> , (September, 2001), 1-12	
		SHERWOOD, TIMOTHY, et al., "Phase Tracking and Prediction", <u>Proceedings of the 30th International Symposium on Computer Architecture (ISCA)</u> , (06/2003), 1-12	

EXAMINER**DATE CONSIDERED**